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PATENT APPLICATION

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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re the Application of

Toshinori NAKAYAMA

Group Art Unit: 2813

Application No.: 09/626,146

Examiner: Nema O. Berezny

Filed: July 26, 2000

Docket No.: 106364

For: SUBSTRATE FOR SEMICONDUCTOR DEVICE, SEMICONDUCTOR CHIP MOUNTING SUBSTRATE, SEMICONDUCTOR DEVICE AND METHOD OF FABRICATION THEREOF, AND CIRCUIT BOARD, TOGETHER WITH ELECTRONIC EQUIPMENT

RESPONSE TO RESTRICTION REQUIREMENT

Director of the U.S. Patent and Trademark Office
Washington, D.C. 20231

Sir:

In reply to the Restriction Requirement mailed August 7, 2001, Applicant provisionally elects Group II, claims 15-21, with traverse.

It is also respectfully submitted that the subject matter of all claims 1-28 is sufficiently related that a thorough search for the subject matter of any one Group of claims would encompass a search for the subject matter of the remaining claims. Thus, it is respectfully submitted that the search and examination of the entire application could be made without serious burden. See MPEP §803 in which it is stated that "if the search and examination of the entire application can be made without serious burden, the Examiner must examine it on the merits even though it includes claims to distinct or independent inventions" (emphasis added). It is respectfully submitted that this policy should apply in the present application in order to avoid unnecessary delay and expense to Applicant and duplicative examination by the Patent Office.

Thus, withdrawal of the Restriction Requirement is respectfully requested.

Respectfully submitted,



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JAO:EDM/gam

Date: September 6, 2001

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